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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
09/922,275	08/06/2001	Kia Silverbrook	YU132US	1826

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SILVERBROOK RESEARCH PTY LTD  
393 DARLING STREET  
BALMAIN, 2041  
AUSTRALIA

EXAMINER

KIM, PETER B

ART UNIT PAPER NUMBER

2851

DATE MAILED: 04/09/2003

Please find below and/or attached an Office communication concerning this application or proceeding.

**Office Action Summary**

Application No.

09/922,275

Applicant(s)

SILVERBROOK, KIA

Examiner

Peter B. Kim

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-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

**Period for Reply**

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If the period for reply specified above is less than thirty (30) days, a reply within the statutory minimum of thirty (30) days will be considered timely.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133).
- Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

**Status**

- 1) ☐ Responsive to communication(s) filed on \_\_\_\_.
- 2a) ☐ This action is **FINAL**. 2b) ☒ This action is non-final.
- 3) ☐ Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

**Disposition of Claims**

- 4) ☒ Claim(s) 1-7 is/are pending in the application.
- 4a) Of the above claim(s) \_\_\_\_ is/are withdrawn from consideration.
- 5) ☐ Claim(s) \_\_\_\_ is/are allowed.
- 6) ☒ Claim(s) 1,2 and 4-7 is/are rejected.
- 7) ☒ Claim(s) 3 is/are objected to.
- 8) ☐ Claim(s) \_\_\_\_ are subject to restriction and/or election requirement.

**Application Papers**

- 9) ☐ The specification is objected to by the Examiner.
- 10) ☒ The drawing(s) filed on 06 August 2001 is/are: a) ☒ accepted or b) ☐ objected to by the Examiner.  
Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).
- 11) ☐ The proposed drawing correction filed on \_\_\_\_ is: a) ☐ approved b) ☐ disapproved by the Examiner.  
If approved, corrected drawings are required in reply to this Office action.
- 12) ☐ The oath or declaration is objected to by the Examiner.

**Priority under 35 U.S.C. §§ 119 and 120**

- 13) ☒ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).  
a) ☒ All b) ☐ Some \* c) ☐ None of:
1. ☐ Certified copies of the priority documents have been received.
2. ☒ Certified copies of the priority documents have been received in Application No. 09/113,053.
3. ☐ Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).
- \* See the attached detailed Office action for a list of the certified copies not received.
- 14) ☐ Acknowledgment is made of a claim for domestic priority under 35 U.S.C. § 119(e) (to a provisional application).  
a) ☐ The translation of the foreign language provisional application has been received.
- 15) ☐ Acknowledgment is made of a claim for domestic priority under 35 U.S.C. §§ 120 and/or 121.

**Attachment(s)**

- 1) ☒ Notice of References Cited (PTO-892)
- 2) ☐ Notice of Draftsperson's Patent Drawing Review (PTO-948)
- 3) ☒ Information Disclosure Statement(s) (PTO-1449) Paper No(s) 2.
- 4) ☐ Interview Summary (PTO-413) Paper No(s) \_\_\_\_.
- 5) ☐ Notice of Informal Patent Application (PTO-152)
- 6) ☐ Other: \_\_\_\_\_

**DETAILED ACTION**

***Claim Rejections - 35 USC § 103***

The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:

(a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negated by the manner in which the invention was made.

Claims 1 and 7 are rejected under 35 U.S.C. 103(a) as being unpatentable over Cyman et al. (Cyman) (US 5,949,438).

Cyman discloses in Fig. 3, an image printing apparatus that comprises a print head (col. 8, lines 26-29), a microcontroller (64) with processor circuitry, print head interface circuitry (600) that is connected between the processor and the print head (Fig. 3) and bus interface that is discrete from the print head interface and is connect to he processor so that the processor can communicate with devices other than the print head via a bus. However, Cyman does not explicitly disclose a microcontroller with the processor circuitry on a wafer substrate. Official Notice is taken that it is well known in the art of microcontrollers and processors to utilize a wafer substrate to position the processor circuitry. Therefore, it would have been obvious to one of ordinary skill in the art at the time of invention to provide the processor circuitry of Cyman on a wafer substrate in order to achieve smaller size and easier manufacturing.

Claim 2 is rejected under 35 U.S.C. 103(a) as being unpatentable over Cyman et al. (Cyman) in view of Granzow (5,751,318).

Cyman discloses in Fig. 3, an image printing apparatus that comprises a print head (col. 8, lines 26-29), a microcontroller (64) with processor circuitry, print head interface circuitry

(600) that is connected between the processor and the print head (Fig. 3) and bus interface that is discrete from the print head interface and is connect to he processor so that the processor can communicate with devices other than the print head via a bus. However, Cyman does not explicitly disclose a microcontroller with the processor circuitry on a wafer substrate. Cyman also does not disclose a page width print head with a plurality of nozzle with micro electromechanical device that is capable of ejecting ink from a nozzle. Official Notice is taken that it is well known in the art of microcontrollers and processors to utilize a wafer substrate to position the processor circuitry. Granzow discloses in Fig. 1, 10 and 14, a page width pring head with a plurality of nozzle arrangements with a micro electromechanical device for ejecting ink (col. 14, lines 8-40, col. 16, lines 24-40). Therefore, it would have been obvious to one of ordinary skill in the art at the time of invention to provide the processor circuitry of Cyman on a wafer substrate in order to achieve smaller size and easier manufacturing. Therefore, it would have been obvious to one of ordinary skill in the art at the time of invention to provide a page width print head of Granzow to the invention of Cyman because of the advantages provided by a page width printhead such as eliminating movement of the printhead and thereby increasing reliability as taught by Granzow in col. 3, lines 56-67.

Claim 4 is rejected under 35 U.S.C. 103(a) as being unpatentable over Cyman et al. (Cyman) in view of Granzow as applied to claim 2 above, and further in view of Lloyd (EPA 0334546).

The further difference between the modified Cyman and the claimed invention is the print head interface configured to define a number of registers for storing clocking and control information. Lloyd discloses in Fig. 1, a print head interface (13) which defines a number of

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registers for storing clocking information to be received by the print head in accordance with a predetermined algorithm (col. 6, lines 23-59). Therefore, it would have been obvious to one of ordinary skill in the art at the time of invention to provide the print head interface which defines a number of registers for storing clocking information because such arrangement can be adjusted routinely at printer start up and/or periodically during operation which helps to accommodate gradual changes in power and avoid adverse effects of heat as taught by Lloyd in col. 5, lines 1-19.

Claims 5 and 6 are rejected under 35 U.S.C. 103(a) as being unpatentable over Cyman et al. (Cyman) in view of Granzow and Lloyd as applied to claim 2 above, and further in view of Kupcho et al. (Kupcho) (5,670,995).

The further difference between the modified Cyman and the claimed invention is the print head interface circuitry connected to an address and data bus which is connected to a central processing unit of the microcontroller and the interface circuitry connected to buffer memory. Kupcho discloses in Fig. 5, the print head interface circuitry (216) connected to the data bus and connected to CPU of microcontroller (computer system, col. 8, lines 60-67) which addresses the registers with the clocking and control information (col. 9, lines 12-40). Kupcho also discloses the memory buffer (220, 218) connected the interface. Therefore, it would have been obvious to one of ordinary skill in the art at the time of invention to provide the data bus and the memory buffer because such arrangement makes an efficient use of computer's computational resources and it also decreases the amount of time required to form an image as taught by Kupcho in col. 3, lines 1-7 and col. 9, lines 41-61.

*Allowable Subject Matter*

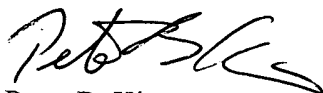
Claim 3 is objected to as being dependent upon a rejected base claim, but would be allowable if rewritten in independent form including all of the limitations of the base claim and any intervening claims.

None of the prior art of record teaches an image sensing and processing apparatus that comprises a VLIW processor circuitry (instead of RISC or CISC processor) with image sensor interface circuitry that is connected between the VLIW processor and the image sensor, and bus interface that is discrete from the image sensor interface circuitry and it connected to the VLIW processor and devices other than the image sensor via a bus.

*Conclusion*

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Peter Kim whose telephone number is (703) 305-0105. The examiner can normally be reached on Monday-Thursday from 6:30 AM to 4:00 PM. The examiner can also be reached on alternate Fridays during the same hours.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Russ Adams, can be reached on (703) 308-2847. Any inquiry of a general nature or relating to the status of this application or proceeding should be directed to the Group receptionist whose telephone number is (703) 308-0956.



Peter B. Kim  
Patent Examiner  
April 2, 2003